LOGIC ANALYSIS DEVICE WITH TRANSMISSION SPEED DISPLAY INDICATORS

ABSTRACT OF THE DISCLOSURE

A logic analysis device with transmission speed display indicators is disclosed. The logic analysis device with transmission speed display indicators is disposed between the computer and the peripheral devices to enable the user to identify the operation status visually, such as, data transmission or volume of traffic during the operation of each peripheral device, at the same time to aid the user for determining whether to activate additional peripheral devices to substantially avoid breakdown and improve the accuracy of data signal transmission.